

FIG. 1

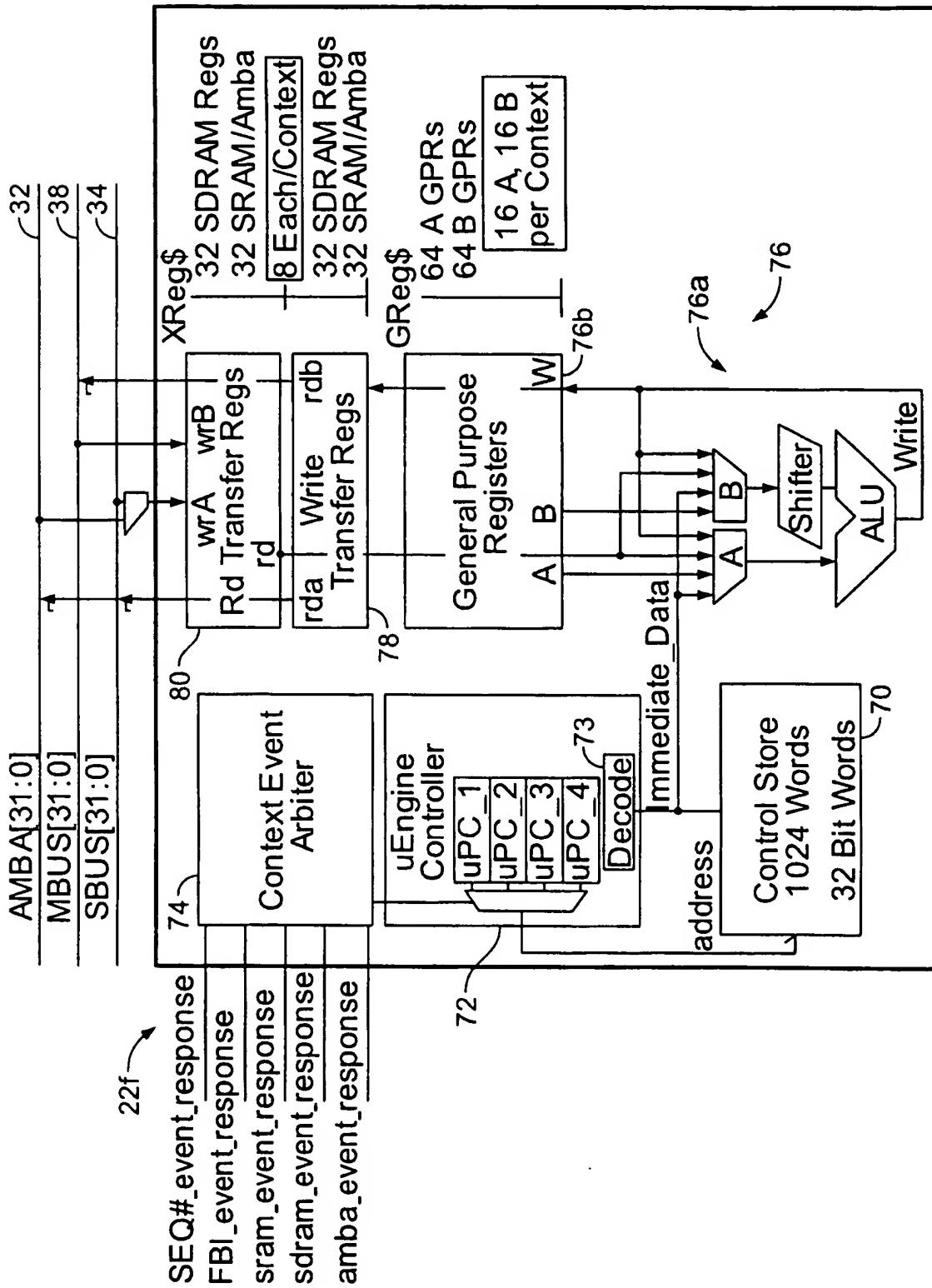


FIG. 2

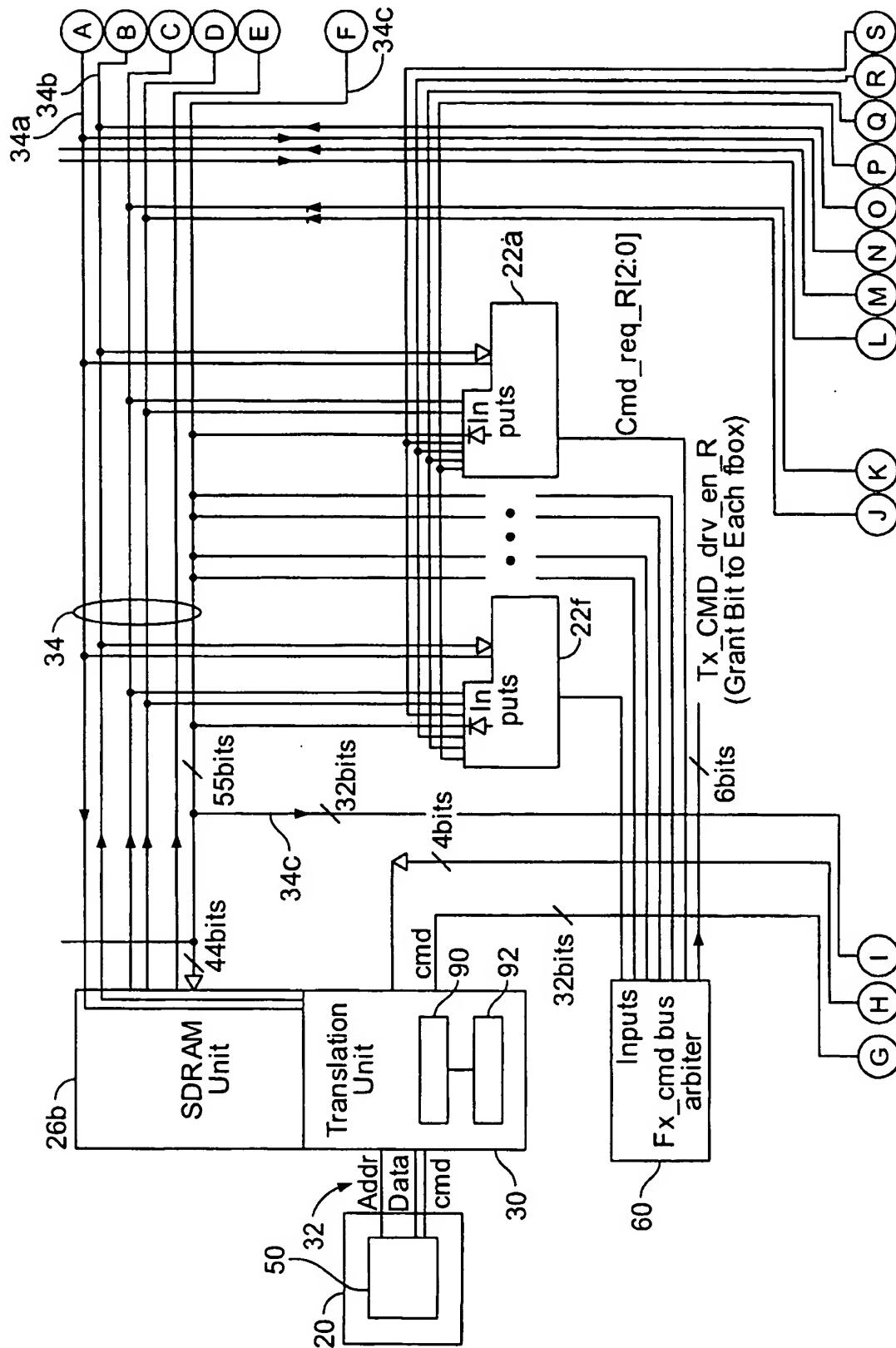


FIG. 3A

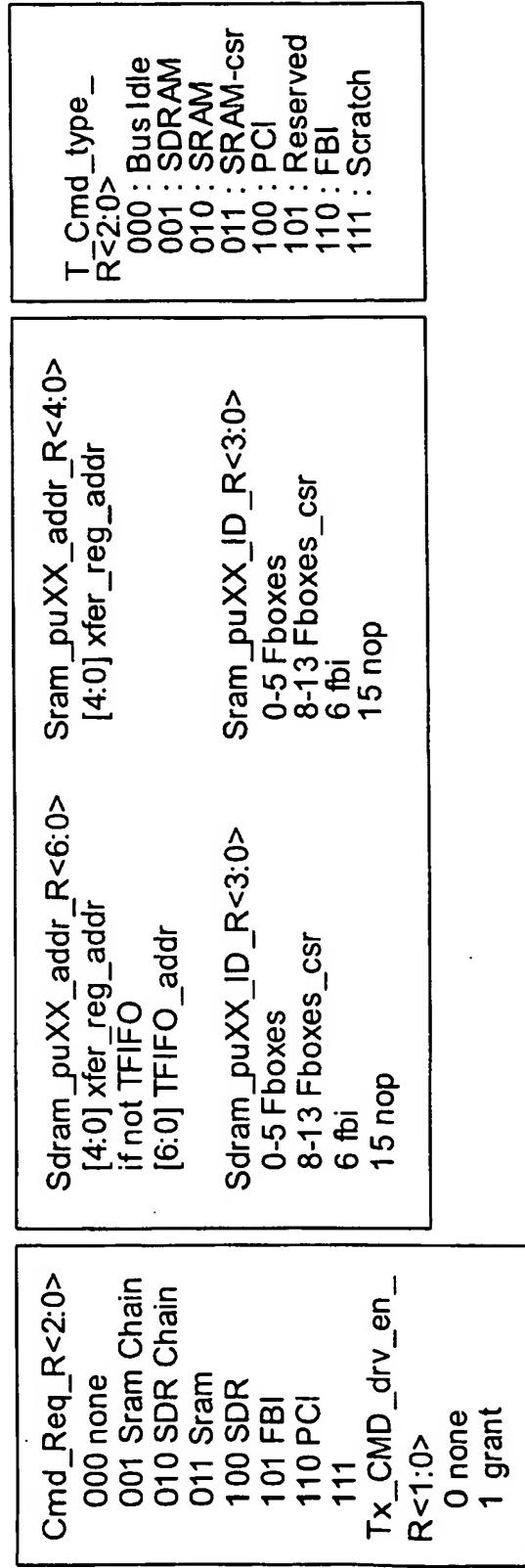
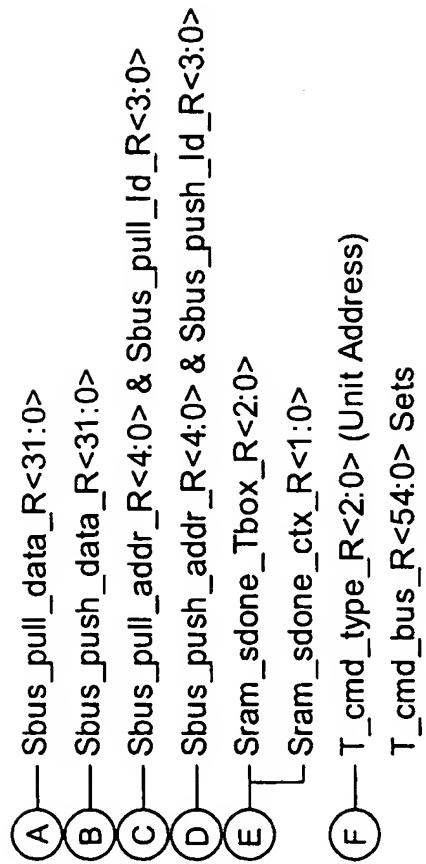
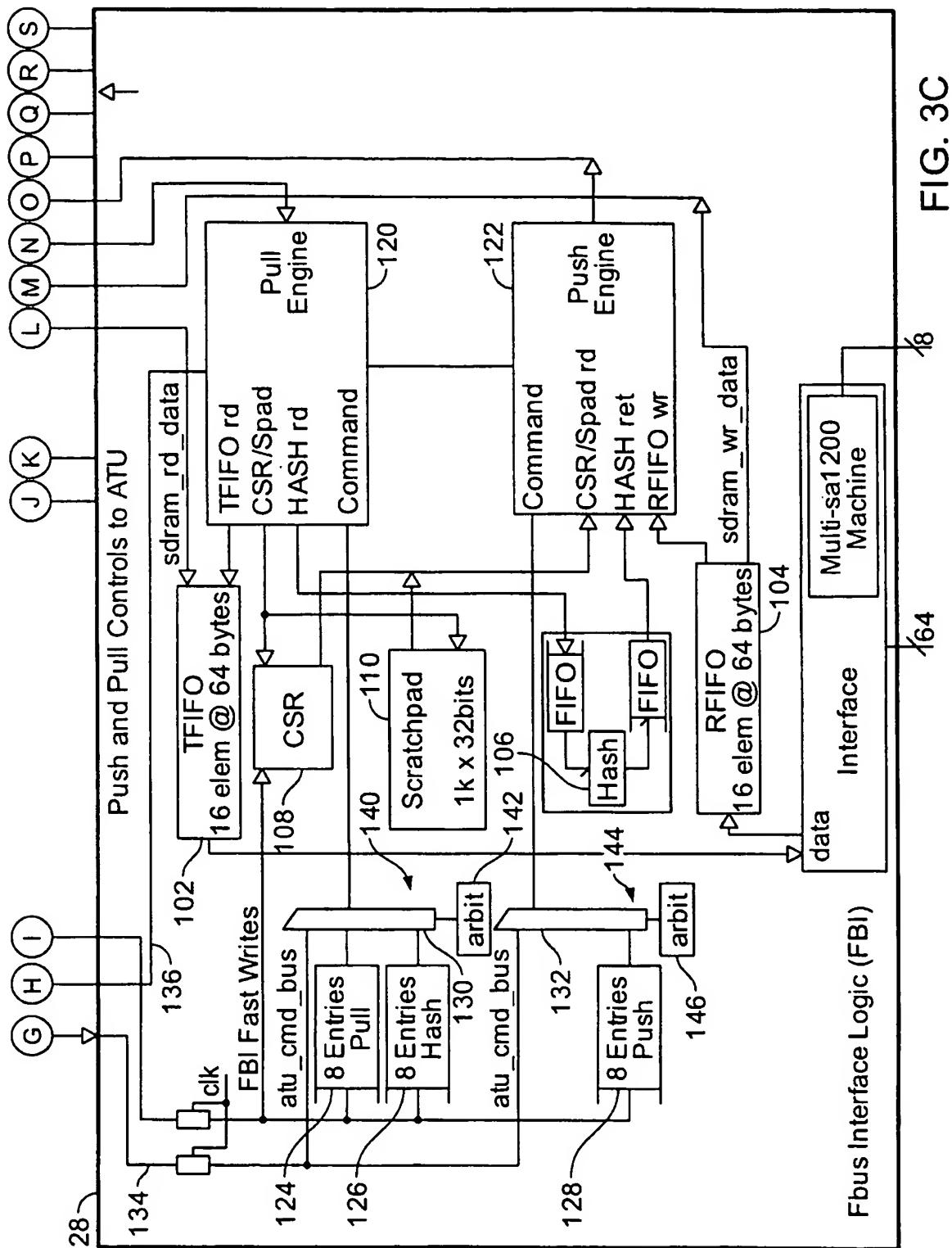


FIG. 3B



ADDRESS SPACE (HEXADECIMAL)		HEXADECIMAL	MAPPED ADDRESSES	XXXX (BINARY)
FFFF FFFF				
E7FF FFFF				
E000 0000				
C000 0000	SDRAM			
B000 0000	TRANSLATION UNIT 30		FBUS INTERFACE (28) SCRATCHPAD (110)	0100 xxxx xxxx xxxx
A000 0000	PCI CONFIG		FBUS INTERFACE(28) REGISTERS (108)	0000 xxxx xxxx xxxx
9000 0000	SYSTEM		MICRO-ENGINE (22a) REGISTERS (78, 80)	0110 1xxx xxxx xxxx
8000 0000	MAC & SRAM CONTROL REGISTERS		MICRO-ENGINE (22b) REGISTERS (78, 80)	0110 0xxx xxxx xxxx
4000 0000	PCI UNIT		MICRO-ENGINE (22c) REGISTERS (78, 80)	0001 1xxx xxxx xxxx
0000 0000	SRAM		MICRO-ENGINE (22d) REGISTERS (78, 70)	0001 0xxx xxxx xxxx
			MICRO-ENGINE (22e) REGISTERS (78, 80)	0000 1xxx xxxx xxxx
			MICRO-ENGINE (22f) REGISTERS (78, 80)	0000 0xxx xxxx xxxx
			MICRO-ENGINE (22a) REGISTERS (76b)	0010 1xxx xxxx xxxx
			MICRO-ENGINE (22b) REGISTERS (76b)	1101 0xxx xxxx xxxx
			MICRO-ENGINE (22c) REGISTERS (76b)	0001 1xxx xxxx xxxx
			MICRO-ENGINE (22d) REGISTERS (76b)	0001 0xxx xxxx xxxx
			MICRO-ENGINE (22e) REGISTERS (76b)	0000 1xxx xxxx xxxx
			MICRO-ENGINE (22f) REGISTERS (76b)	0000 0xxx xxxx xxxx

FIG. 4

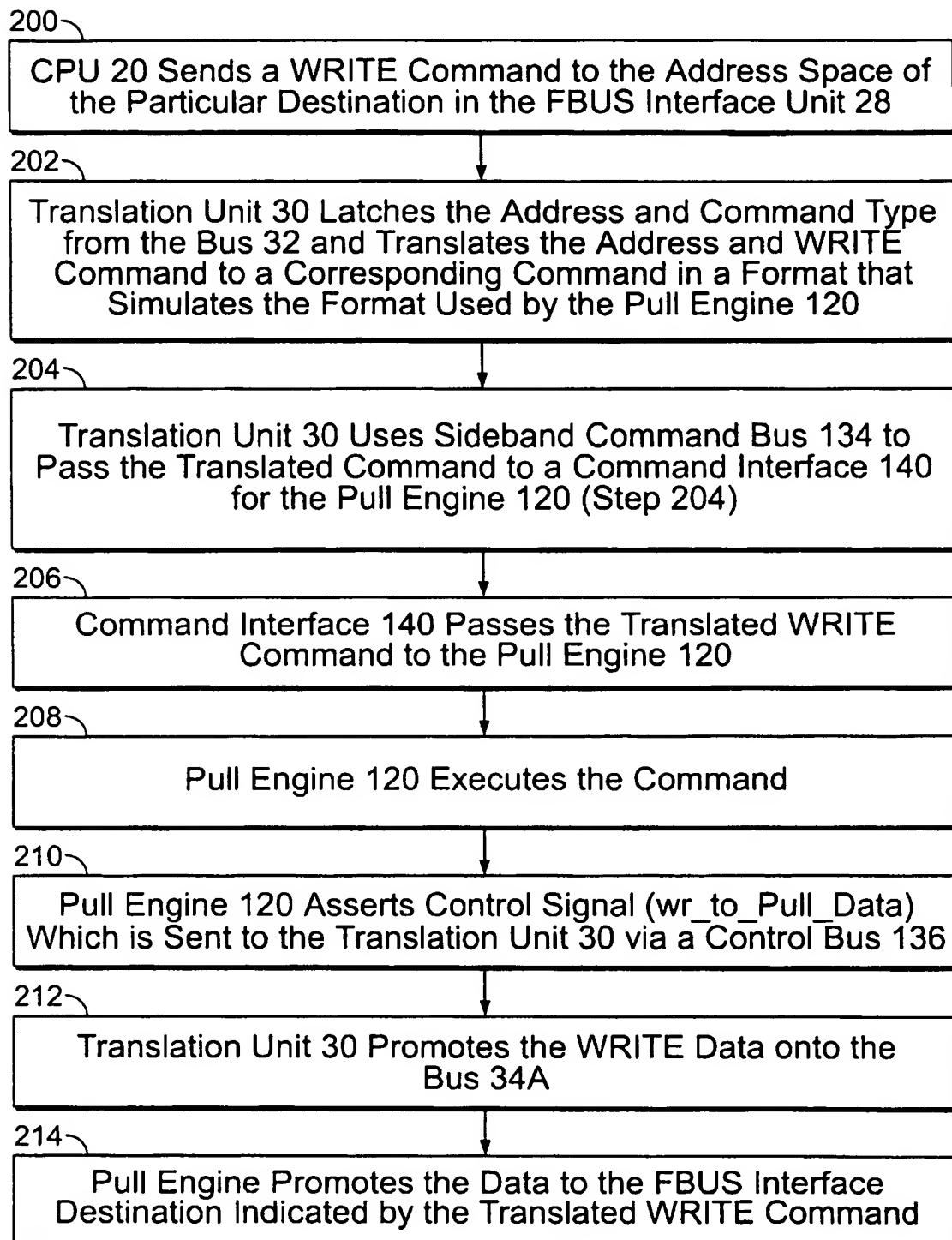


FIG. 5

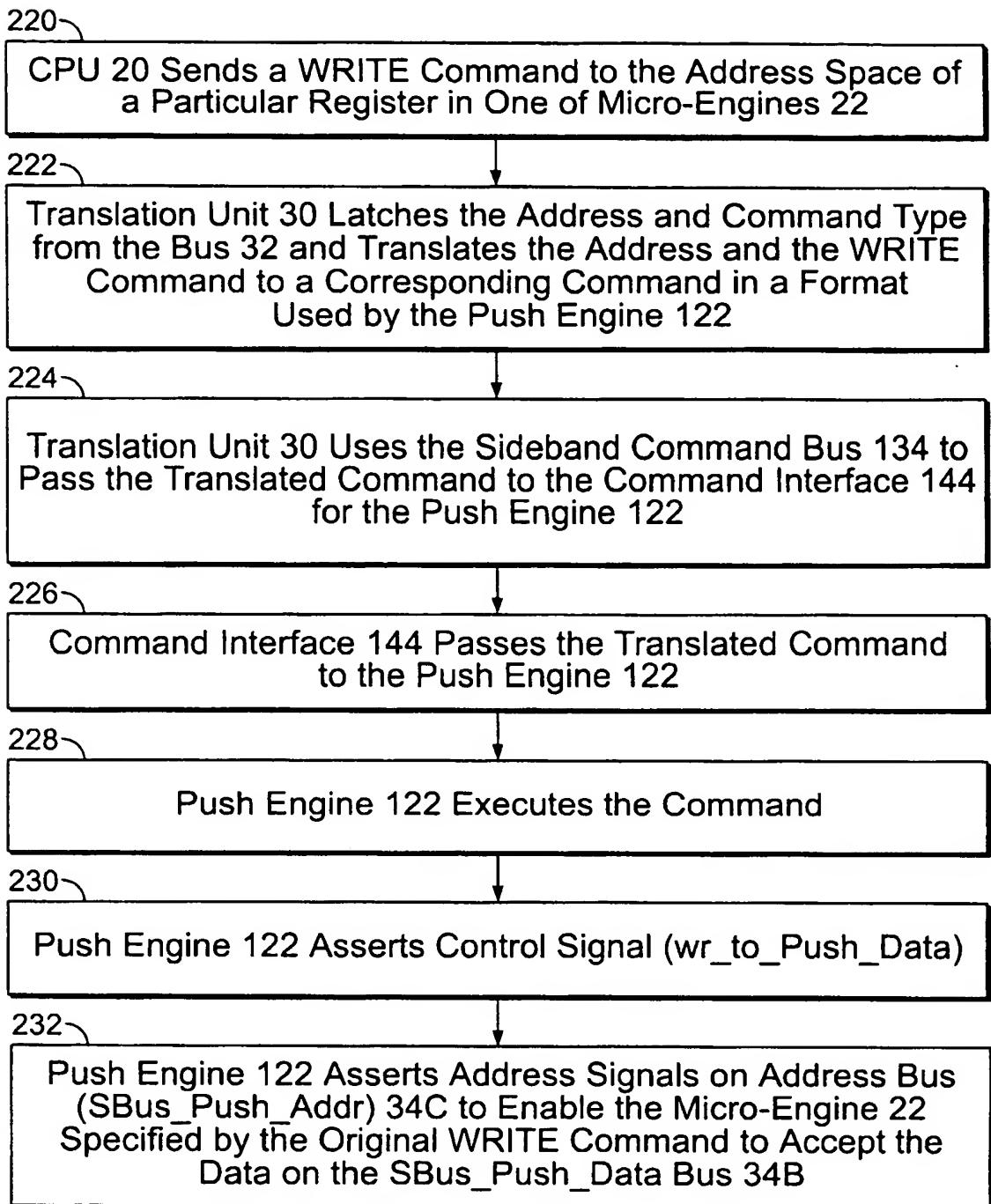


FIG. 6

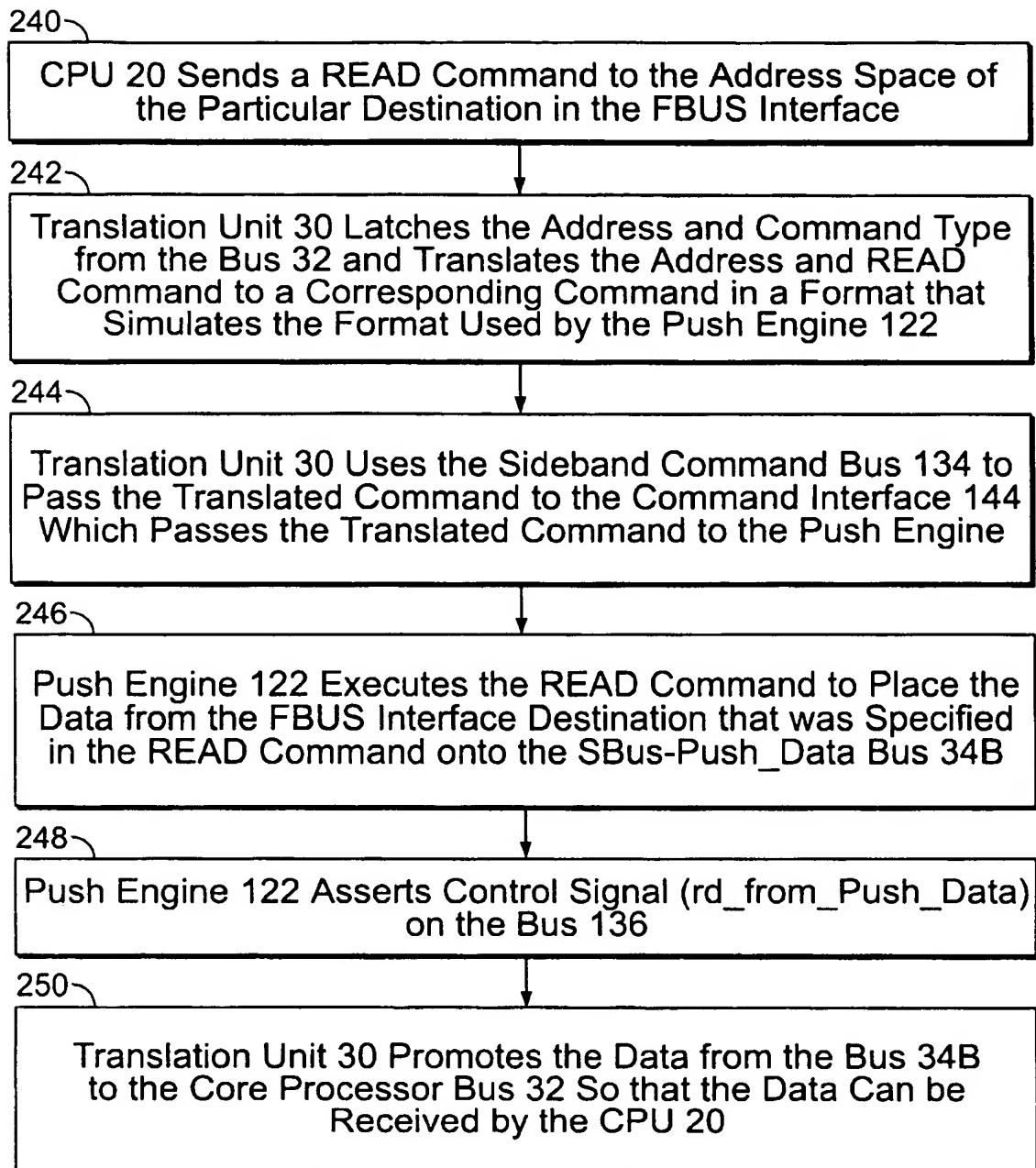


FIG. 7

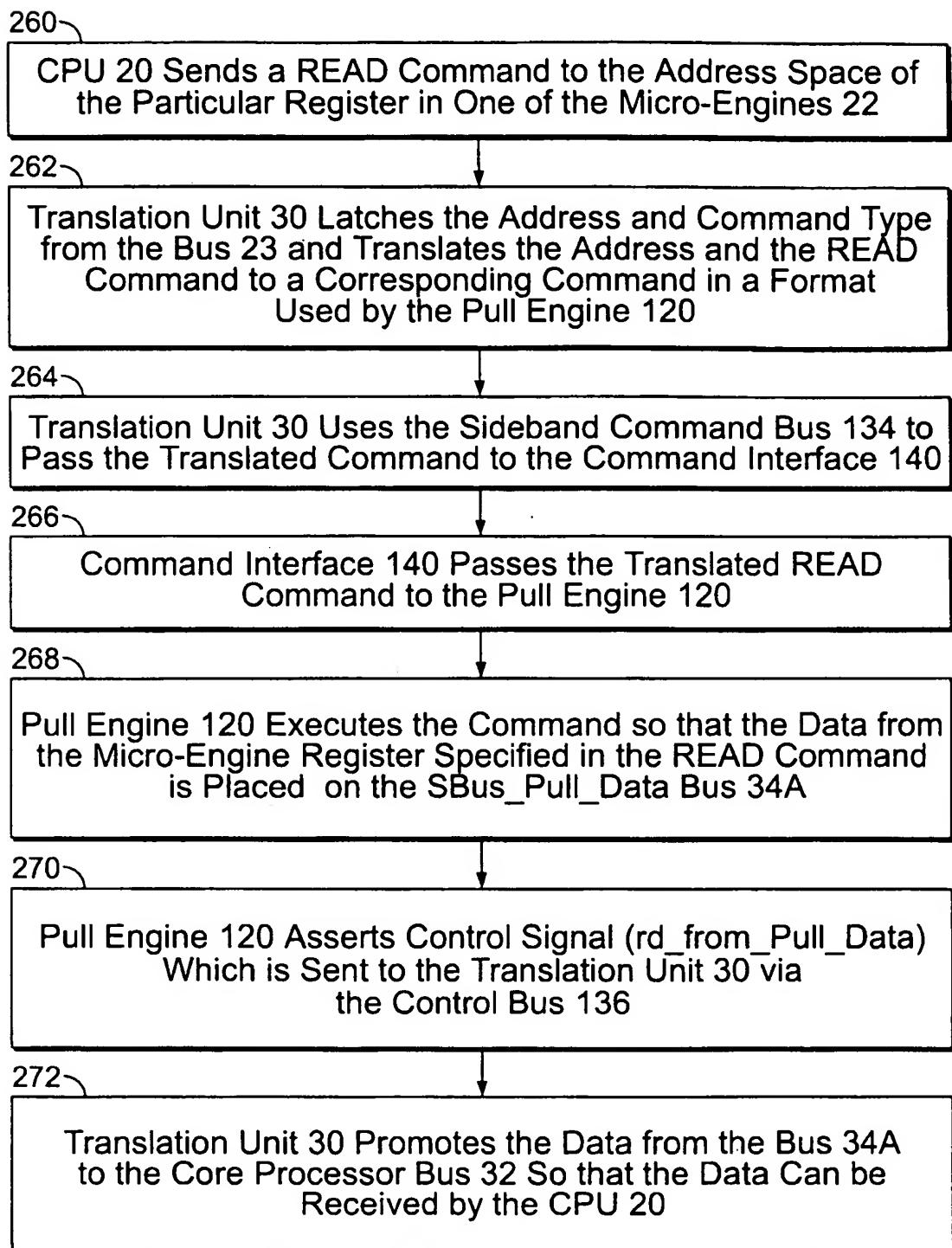


FIG. 8